

1 CLAIMS

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4 1. In a PLD having a JTAG port, a decryptor for decrypting an encrypted
5 bitstream, and a memory for storing decryption keys, a method for loading the
6 keys comprising:

7 placing the PLD into a printed circuit board;
8 testing the printed circuit board using the JTAG port of the PLD; and
9 loading the decryption keys into the memory using the JTAG port.

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11 2. The method of Claim 1 wherein the step of loading the decryption keys into the
12 memory is performed without also loading a design into the PLD.

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14 3. The method of Claim 1 wherein the step of loading the decryption keys into the
15 memory is performed without the use of a device programmer.

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17 4. A PLD comprising:

18 a test access port for testing the PLD; and
19 a circuit for loading at least one decryption key through the test access port.

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21 5. The PLD of Claim 4 wherein the test access port is a JTAG port.